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Assistant Commissioner for Patents  
Washington, D.C. 20231

On Aug. 16, 2000  
By: G. Aka

PATENT  
Attorney Docket No.: 19705-000100US

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TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

**Bulent Dervisoglu et al.**

Application No.: 09/275,726

Filed: March 24, 1999

For: ON-CHIP SERVICE PROCESSOR  
FOR TEST AND DEBUG OF  
INTEGRATED CIRCUITS

Examiner: A. Decayd **RECEIVED**  
Art Unit: 2812 SEP 1 2000  
**Group 2700**

SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT UNDER  
37 CFR §1.97 and §1.98

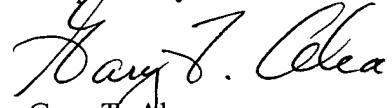
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The references cited on attached form PTO-1449 are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

  
Gary T. Aka  
Reg. No. 29,038

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TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, 8<sup>th</sup> Floor, San Francisco, CA 94111-3834  
Tel: 650-326-2400 / Fax: 650-326-2422  
GTA:gjs  
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